

(F)

Atty. Docket No. NLMI.P195

PATENT

IN THE UNITED STATES PATENT OFFICE

In Re Patent Application of

First Named Inventor: Srinivasan, Varadarajan

Application No.: 10/613,629

Filed: 7/2/2003

For: METHOD AND APPARATUS FOR MANAGING  
INDIVIDUAL TRAFFIC FLOWS

Examiner:

Art Unit: 2121

Confirmation 4352  
No.

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Commissioner for Patents  
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INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.97

Sir:

Enclosed is an Information Disclosure Citation Form 1449/PTO together with a copy of each reference cited therein, excluding U.S. Patents and Published U.S. Patent Applications. It is respectfully requested that the cited references be considered and that the enclosed copy of the Form 1449/PTO be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant.

This Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

  X   37 C.F.R. §1.97(b).

       37 C.F.R. §1.97(c).

       Authorization is hereby given to charge Deposit Account No. 501914 for the fee of \$180.00 under 37 C.F.R. §1.17(p) for the submission of this Information Disclosure Statement; or

       A statement pursuant to 37 C.F.R. §1.97(e) is attached hereto.

\_\_\_\_\_ 37 C.F.R. §1.97(d). Authorization is hereby given to charge Deposit Account No. 501914 for the fee of \$180.00 and a statement pursuant to 37 C.F.R. §1.97(e) is enclosed.

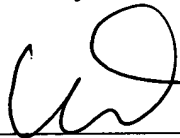
Pursuant to 37 C.F.R. 1.97(h), the submission of this Information Disclosure Statement is not to be construed as an admission that the information cited in this statement is material to patentability.

The Commissioner is hereby authorized to charge any fee deficiency in connection with this submission to Deposit Account No. 501914.

Respectfully submitted,

Date

12/7/2006

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William L. Paradice, Reg. No. 38,990  
Tel. 408-236-6646

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	<b>Filed</b>	7/2/2003
	<b>First Inventor</b>	Srinivasan, Varadarajan
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Non Patent Literature Documents		
Examiner Initials	Name of Author, Title of Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country where Published	Trans- lation
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	Agere Ads Additional PaloadPlus Processor to Product Line, Agere Press Release, November 29, 2000 (11/29/00), 3 pgs.	
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	ATLAS I: A General-Purpose, Single-Chip ATM Switch with Credit-Based Flow Control, IEEE Hot Interconnects IV Symposium Proceedings, Stanford, CA, Pub. August 15-17, 1996, 11 pgs.	
	CSIX-L1: Common Switch Interface Specification-L1, published by CSIX, August 5, 2000(8/5/2000) , 72 pgs.	

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	Implementation of ATLASI: a Single-Chip ATM Switch with Backpressure, IEEE Hot Interconnects IV Symposium Proceedings, Stanford, CA, Pub. August 13-15, 1998, 12 pgs.	
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	Zettacom: Hurry Up and Wait, www.lightreading.com, Pub. Nov. 29, 2000 (11/29/00), 2 pgs.	

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